ECE/CS 5745/6745: Testing & Verification of Digital Circuits

Prepared by *Priyank Kalla*

Fall 2023, Homework # 1

Due Date: Wednesday Sept. 6, 11:59pm, on Canvas.

Note: The HWs should be uploaded by students electronically on Canvas.

1) (25 points) Are the following statements TRUE or FALSE? If the statement is TRUE, prove it. Otherwise, show a counter-example. No points for just stating TRUE, FALSE.
   a) The Shannon's expansion of f w.r.t. variable x can be given as $f = (x + f_{\overline{x}})(\overline{x} + f_x)$.
   b) The Shannon's expansion of f w.r.t. x can also be given as $f = x \cdot f_x \oplus \overline{x} \cdot f_{\overline{x}}$, where $\oplus$ denotes the XOR operation.
   c) $\overline{f} = x \cdot \overline{(f_x)} + \overline{x} \cdot \overline{(f_{\overline{x}})}$.
   d) Let f be a Boolean function, and x be a variable in its support. To check whether f is TAUTOLOGY, it is *necessary and sufficient* to check if both its cofactors $f_x, f_{x'}$ are TAUTOLOGY.
   e) Given a Boolean function f that is *positive unate* in variable x. To check if f is TAUTOLOGY, it is *sufficient* to check that its negative cofactor $f_{x'}$ is TAUTOLOGY.
2) (15 points) For the circuit shown in Fig. 1, using the method of Boolean differences, identify the set of all assignments to the input variables that allow to propagate the changes on signal a to the output Z.

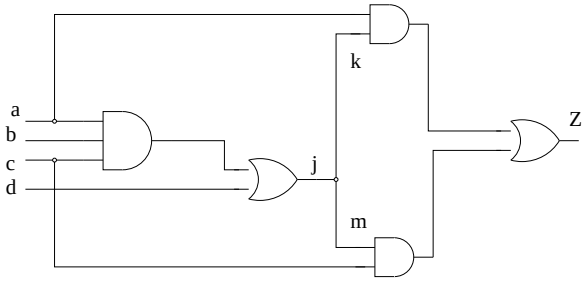

Fig. 1: Is Z sensitive to changes in a?

3) (20 points) We know that the Shannon's expansion of f w.r.t. variable x is given as $f = x \cdot f_x + x' \cdot f_{x'}$. This expansion corresponds to an AND-OR-NOT representation. Notice that the Boolean operators in Shannon's expansion include AND, OR as well as NOT gates. You

also know that any Boolean function can be implemented using AND-OR-NOT gates (universal logic); thus, Shannon's expansion can also be universally applied.

- Starting from the Shannon's expansion, you are asked to derive the following expansion, called the *positive-Davio expansion*: $f = f_{x'} \oplus x \cdot (f_x \oplus f_{x'})$, where $\oplus$ represents the XOR operation. Notice that in the Davio's expansion, you only see AND and XOR operations. Some of you will recall that AND-XOR is also universal logic. If you're unaware of AND-XOR being universal logic, then please take a look at the slides on universal logic that I have uploaded on the class webpage at https://my.ece.utah.edu/~kalla/ECE6745/ch2-universal-logic.pdf. You may also watch the corresponding video in the Media Gallery on Canvas. Davio expansion can be used to implement any Boolean function using AND and XOR gates only.
- Implement the Boolean function $f = ab + \overline{a}c$ as a logic circuit using only AND and XOR gates.
- Derive the *negative Davio* decomposition: $f = f_x \ \oplus \ \overline{x} \cdot (f_x \oplus f_{x'})$.

4) (10 points) Let $f = a'b'c' + a'b'c + a'bc' + ab'c'$. Identify the component of the function f that is independent of variable b. What is this operation called?
5) (10 points) Given the function $f = a'b'c' + a'b'c + a'bc' + ab'c'$ (the same as above), find the smallest function, larger than f, that contains f, but does not contain the variable b in its support. What is this operation called?
6) (20 points) Let $f = a'b' + b'c + ab$. Let $g = ac$. You are asked to check if $g \subset f$? Using an appropriate formulation that we have discussed in class, perform this containment check. Describe your formulation, show your work and state your answer whether $g \subset f$. [Hint: Remember, I showed you in class, the relationship between containment and tautology?]