

Arduino Code (STP08DP05)

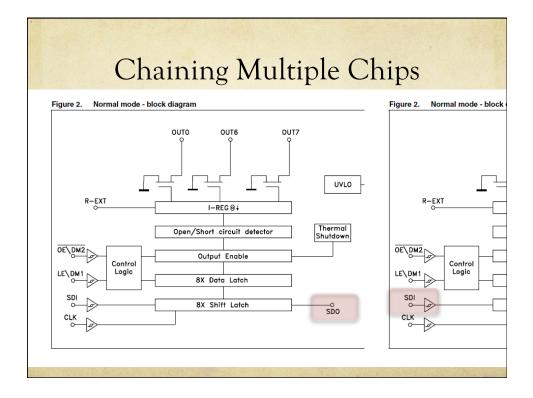
int latchPin = 8; //Pin connected to LE of STP08DP05
int clockPin = 12; //Pin connected to CLK of STP08DP05
int dataPin = 11; //Pin connected to SDI of STP08DP05
Int OEPin = 10; //Pin connected to OEbar of STP08DP05

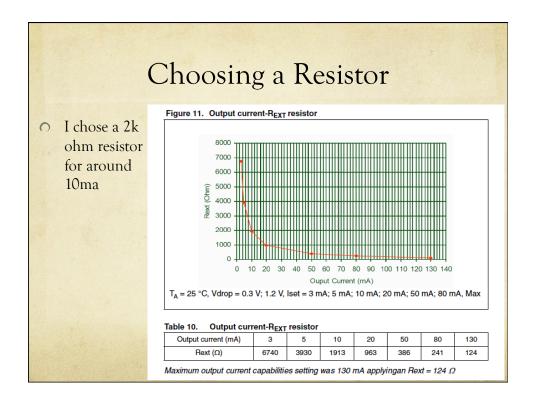
void setup() { //set pins to output because they are addressed in the main loop pinMode(latchPin, OUTPUT); pinMode(clockPin, OUTPUT); pinMode(dataPin, OUTPUT); pinMode(OEPin, OUTPUT);}

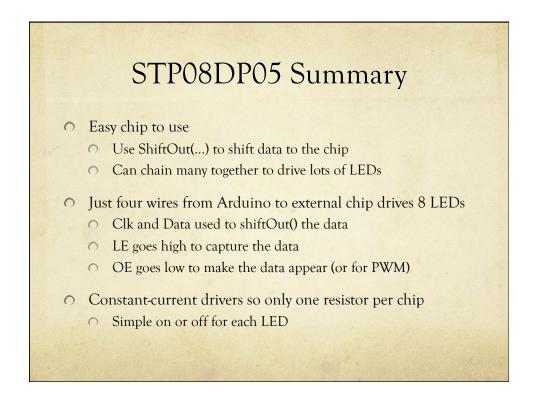
void loop() { //count up routine

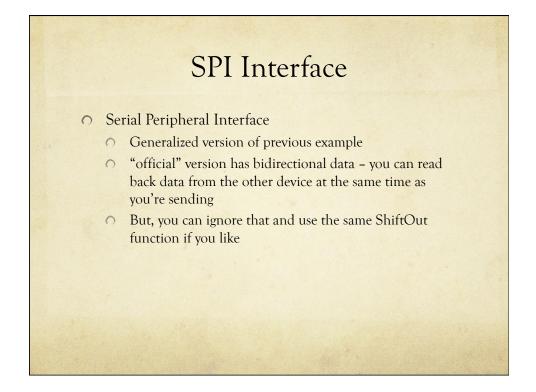
for (int j = 0; j < 256; j++) {
 //ground latchPin and hold low for as long as you are transmitting, OE pin is high...
 digitalWrite(latchPin, LOW); digitalWrite(OEPin, HIGH);
 shiftOut(dataPin, clockPin, LSBFIRST, j);</pre>

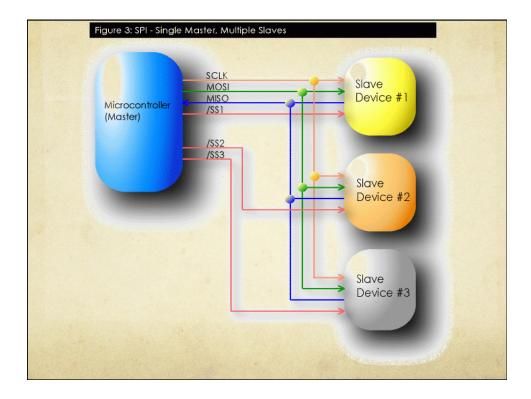
//return the latch pin high transfer data to output latch, OE low to light the LEDs
digitalWrite(latchPin, HIGH); digitalWrite(OEPin, LOW);
delay(1000); }}

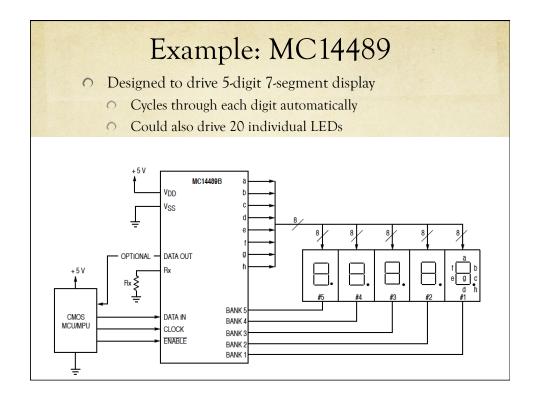




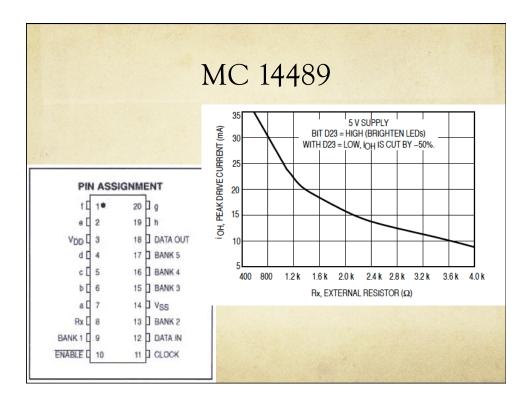


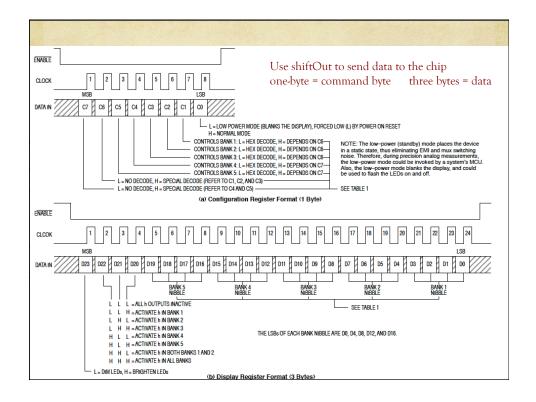


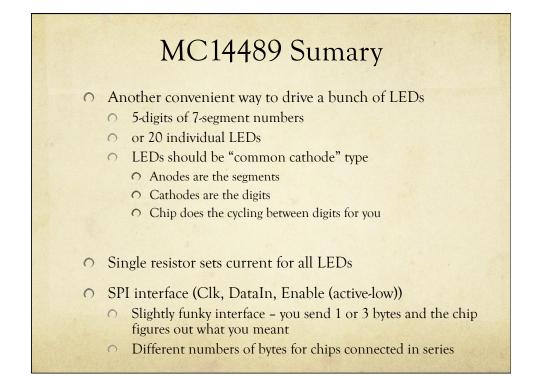


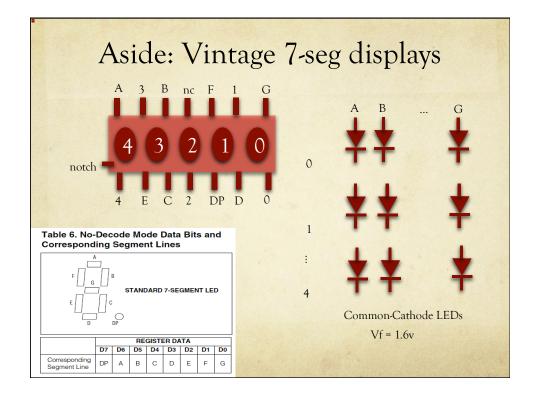


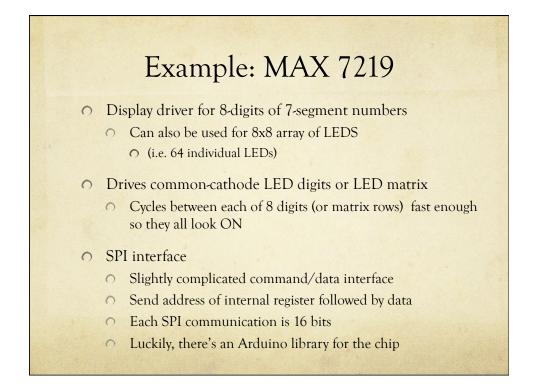
| Example: | | | | | | | | |
|-------------------------------|-------------|--------------------|--|---|----|--------|-----------------------------|----|
| L'Ampie. | | | | | L | amp Co | ondition | 15 |
| MC14489 | Bank Nit | ble Value | 7–Segme Chara | nt Display acters | | (Invok | ecode ed via 1 to C7) | |
| • Send in four bits | Hexadecimal | Binary MSB LSB | Hex Decode (Invoked via Bits C1 to C5) | Special Decode (Invoked via Bits C1 to C7) | d | c | ь | а |
| per digit | \$0 | | 0 | | | | | |
| | \$1 | гггн | | c H | | | | on |
| • Three decoding | \$2 \$3 | L L H L L L H H | 2 3 | н К | | | on | |
| modes | \$3 | | Ч | J | | on | on | on |
| O Hex | \$5 | | | 1 | | on | | on |
| | \$6 | гннг | 8 | 0 | | on | on | |
| • Special | \$7 | L Н Н Н | 7 | 0 | | on | on | on |
| No Decode | \$8 | нцц | 83 | P | on | | | |
| | \$9 | нссн | 9 ④ | r | on | | | on |
| | \$A | нгнг | 8 | U | on | | on | |
| | \$B | нгнн | Ь | U | on | | on | on |
| | \$C | ннці | E | У | on | on | | |
| and the second second | \$D | ннгн | 6 | - | on | on | | on |
| | \$E | н н н ц | E F | - | on | on | on | |
| | \$F | нннн | Г | 0 | on | on | on | on |

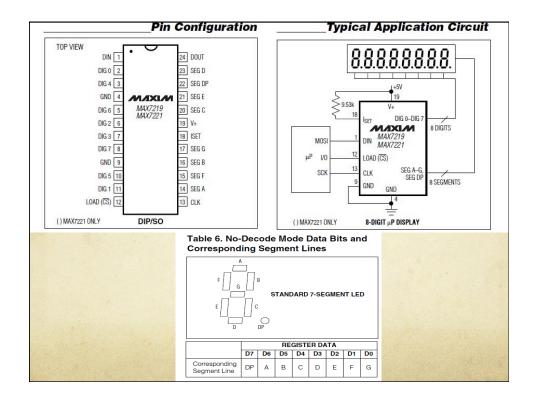


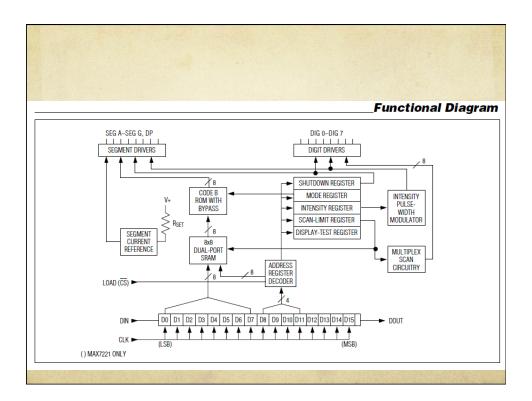


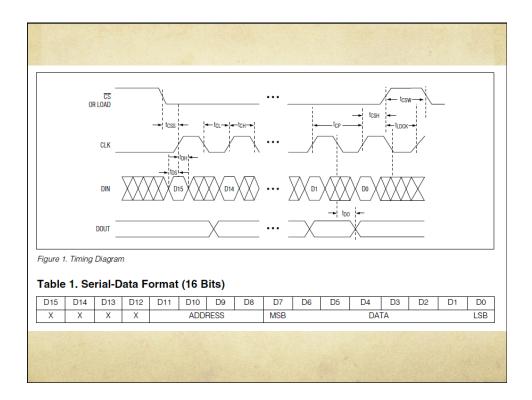




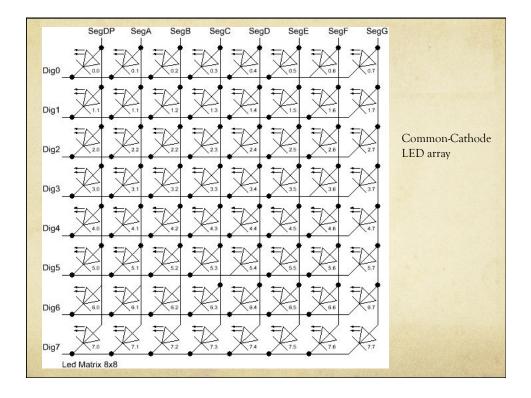








| REGISTER | | ADDRESS | | | | | | |
|-----------------|-------------|---------|-----|----|----|-------------|--|--|
| | D15- D12 | D11 | D10 | D9 | D8 | HEX CODE | | |
| No-Op | Х | 0 | 0 | 0 | 0 | 0xX0 | | |
| Digit 0 | Х | 0 | 0 | 0 | 1 | 0xX1 | | |
| Digit 1 | Х | 0 | 0 | 1 | 0 | 0xX2 | | |
| Digit 2 | Х | 0 | 0 | 1 | 1 | 0xX3 | | |
| Digit 3 | Х | 0 | 1 | 0 | 0 | 0xX4 | | |
| Digit 4 | Х | 0 | 1 | 0 | 1 | 0xX5 | | |
| Digit 5 | Х | 0 | 1 | 1 | 0 | 0xX6 | | |
| Digit 6 | Х | 0 | 1 | 1 | 1 | 0xX7 | | |
| Digit 7 | Х | 1 | 0 | 0 | 0 | 0xX8 | | |
| Decode Mode | x | 1 | 0 | 0 | 1 | 0xX9 | | |
| Intensity | Х | 1 | 0 | 1 | 0 | 0xXA | | |
| Scan Limit | Х | 1 | 0 | 1 | 1 | 0xXB | | |
| Shutdown | Х | 1 | 1 | 0 | 0 | 0xXC | | |
| Display Test | x | 1 | 1 | 1 | 1 | 0xXF | | |



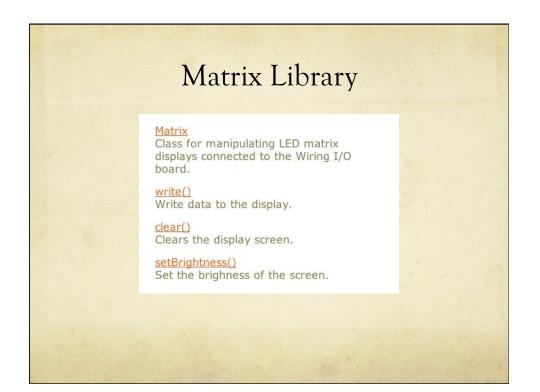
MAX 7219

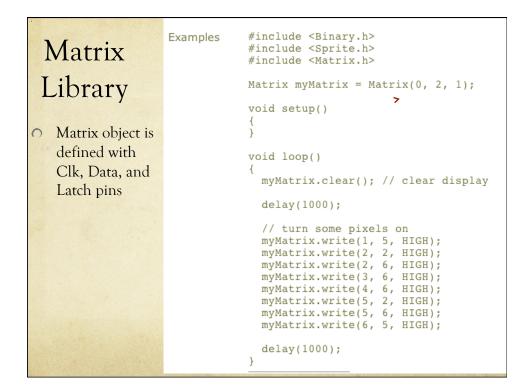
• On the one hand – just like MC14489

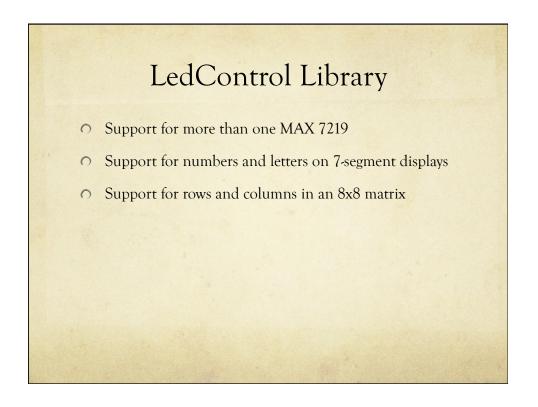
On the other hand, more complex internal structure
 Each SPI transfer needs to be 16 bits – address/data

• Two Arduino libraries available

- Matrix built-in to Arduino environment
- LedControl download from Playground more complex control







LedControl Library

/* We start by including the library */ #include "LedControl.h"

/* Make a new instance of an LedControl object

* Params :

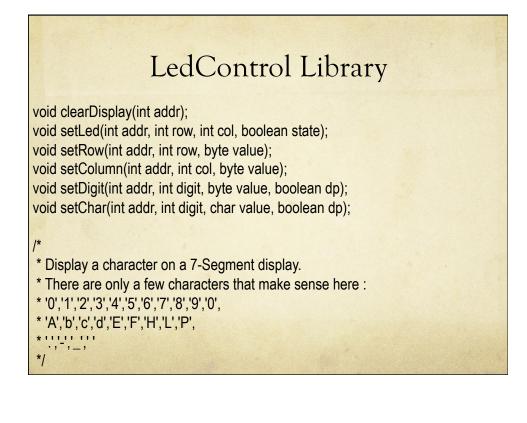
* int dataPin The pin on the Arduino where data gets shifted out (Din on MAX)

* int clockPin The pin for the clock (CLK on MAX)

* int csPin The pin for enabling the device (LD/CS on MAX)
 * int numDevices The maximum number of devices that can be controlled

*/

LedControl Ic1=LedControl(12,11,10,1);



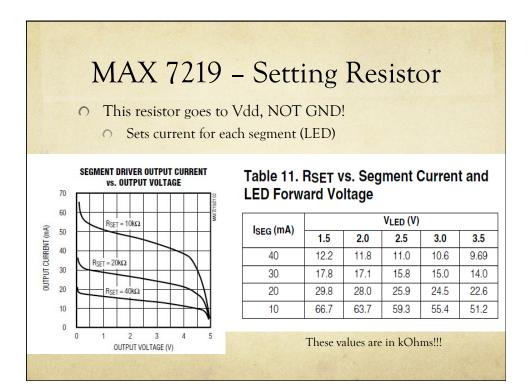
LedControl Library

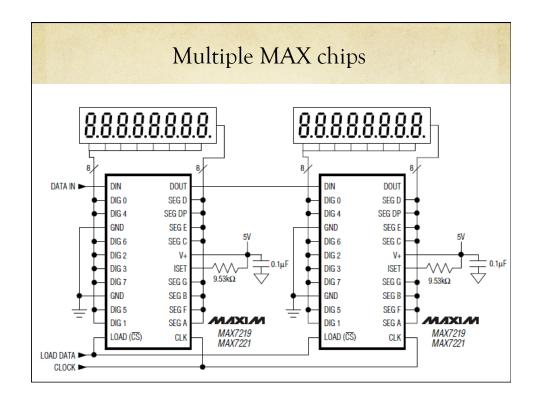
//include this file so we can write down a byte in binary encoding #include <binary.h>

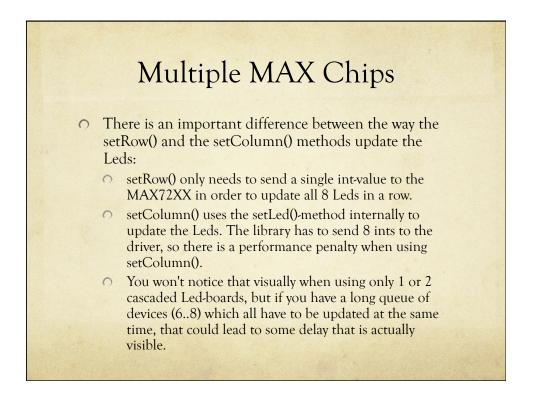
//now setting the leds in the sixth column on the first device is easy lc.setColumn(0,5,B00001111);

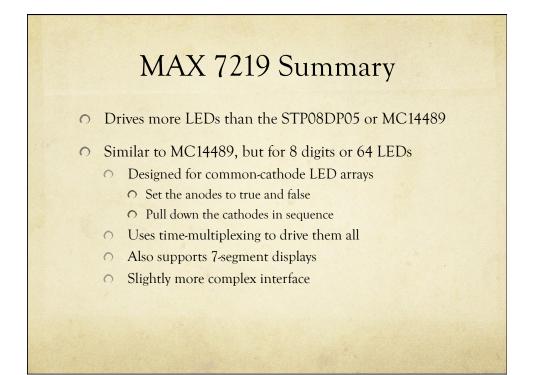
//now setting the leds from the third row on the first device is easy lc.setRow(0,2,B10110000);

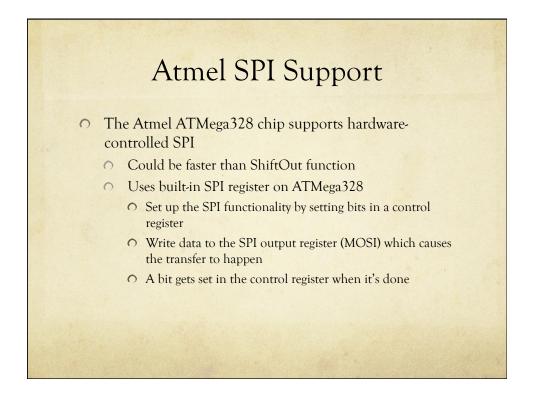
//switch on the led in the 3'rd row 8'th column
//and remember that indices start at 0!
Ic.setLed(0,2,7,true);
//Led at row 0 second from left too
Ic.setLed(0,0,1,false);

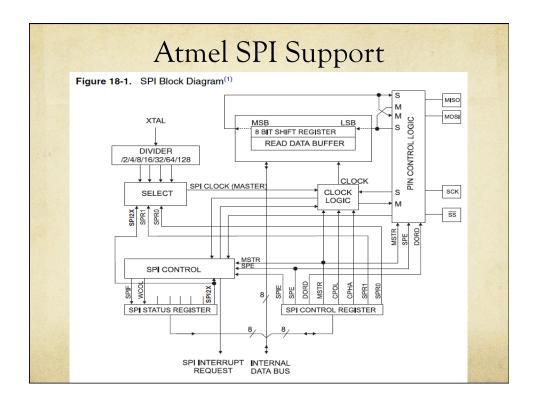


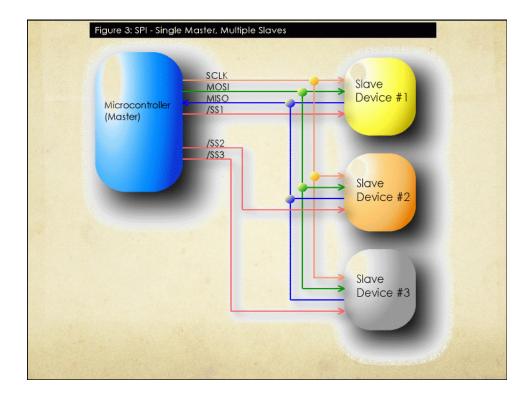












SPI library setup

Spi Library

This library provides functions for transferring information using the Serial Peripheral Interface (SPI). The SPI interface is automatically initialized when the Spi library is included in a sketch. It sets the following digital I/O pins:

| pin | 13 | SCK | SPI | clock |
|-----|----|------|-----|----------------------|
| pin | 12 | MISO | SPI | master in, slave out |
| pin | 11 | MOSI | SPI | master out, slave in |
| pin | 10 | SS | SPI | slave select |

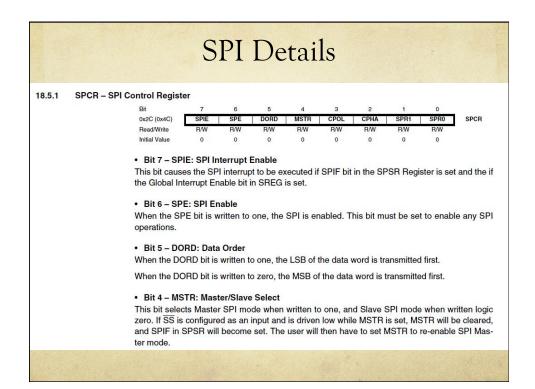
The default SPI configuation is as follows:

SPI Master enabled MSB of the data byte transmitted first SPI mode 0 (CPOL = 0, CPHA = 0) SPI clock frequency = system clock / 4

| mode(byte config) Sets the SPI configuration register. Only required if the default configuration described above must be modified. The SPE (SPI enabled) and MSTR (SPI master) bits are always set. If there are multiple SPI devices on the bus which require different SPI configurations, this function can be called before accessing each different device type to set the appropriate configuration. |
|---|
| Example: |
| Spi.mode((1< <cpol) (1="" 3<="" <<="" cpha));="" mode="" set="" spi="" th="" =""></cpol)> |
| Spi.mode((< <spr0)); 16<="" clock="" set="" spi="" system="" th="" to=""></spr0));> |
| byte transfer(byte b) |
| |
| Sends and receives a byte from the SPI bus. |
| |
| Example: |
| n = Spi.transfer(0x2A); // sends the byte 0x2A |
| // and returns the byte received |
| byte transfer(byte b, byte delay) |
| Delays for a number of microseconds, then sends and receives a byte from |
| |
| the SPI bus. This function is used if there are timing considerations |
| associated with the data transfer. |
| Example: |
| |
| n = Spi.transfer(0x2A, 2); // waits 2 usec, then sends the byte 0x2A |
| // and returns the byte received |



Magic stuff happens here: By writing data to the SPDR register, the SPI transfer is Started. When the transfer is complete, the system raises the SPIF bit in the SPSR Status register. The data that comes back from the slave is in SPDR when you're Finished.



SPI Details

• Bit 3 – CPOL: Clock Polarity

When this bit is written to one, SCK is high when idle. When CPOL is written to zero, SCK is low when idle. Refer to Figure 18-3 and Figure 18-4 for an example. The CPOL functionality is summarized below:

Table 18-3. CPOL Functionality

| CPOL | Leading Edge | Trailing Edge |
|------|--------------|---------------|
| 0 | Rising | Falling |
| 1 | Falling | Rising |

• Bit 2 - CPHA: Clock Phase

The settings of the Clock Phase bit (CPHA) determine if data is sampled on the leading (first) or trailing (last) edge of SCK. Refer to Figure 18-3 and Figure 18-4 for an example. The CPOL functionality is summarized below:

Table 18-4. CPHA Functionality

| СРНА | Leading Edge | Trailing Edge |
|------|--------------|---------------|
| 0 | Sample | Setup |
| 1 | Setup | Sample |
| | | |

SPI Details

· Bits 1, 0 - SPR1, SPR0: SPI Clock Rate Select 1 and 0

These two bits control the SCK rate of the device configured as a Master. SPR1 and SPR0 have no effect on the Slave. The relationship between SCK and the Oscillator Clock frequency f_{osc} is shown in the following table:

Table 18-5. Relationship Between SCK and the Oscillator Frequency

| SPI2X | SPR1 | SPR0 | SCK Frequency |
|-------|------|------|-----------------------|
| 0 | 0 | 0 | f _{osc} /4 |
| 0 | 0 | 1 | f _{osc} /16 |
| 0 | 1 | 0 | f _{osc} /64 |
| 0 | 1 | 1 | f _{osc} /128 |
| 1 | 0 | 0 | f _{osc} /2 |
| 1 | 0 | 1 | f _{osc} /8 |
| 1 | 1 | 0 | f _{osc} /32 |
| 1 | 1 | 1 | f _{osc} /64 |
| | | | • |

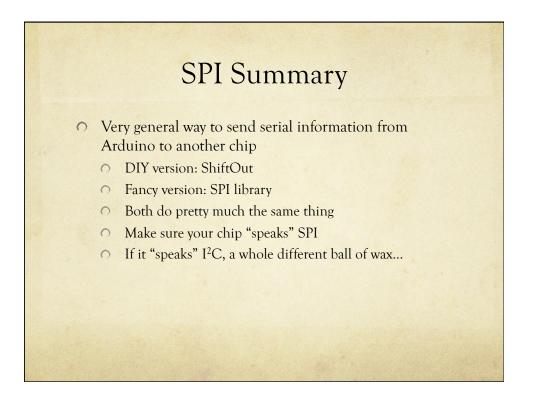
| | | | SF | PII | Deta | ails | | | | |
|---------|---|-------------------------|----------------------------|-----------------------|-------------|------------|----------|-----------|----------|--------|
| SR – SP | I Status Register | r | | | | | | | | |
| | Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | |
| | 0x2D (0x4D) | SPIF | WCOL | - | 12 | - | - | - | SPI2X | SPSR |
| | Read/Write | R | R | R | R | R | R | R | R/W | 1 |
| | Initial Value | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | |
| | • Bit 7 – SP When a seria SPCR is set a | al transfe and globa | er is comp al interrupt | lete, the s are en | abled. If 3 | SS is an i | nput and | is driven | low wher | the SI |
| | in Master mo correspondin | | | | 0 | | | | | |
| | SPI Status R | eaister w | ith SPIF s | et. then | accessin | the SPI | Data Re | aister (S | PDR) | |

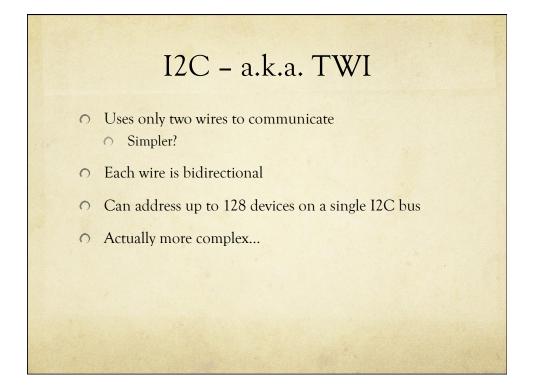
• Bit 6 - WCOL: Write COLlision Flag

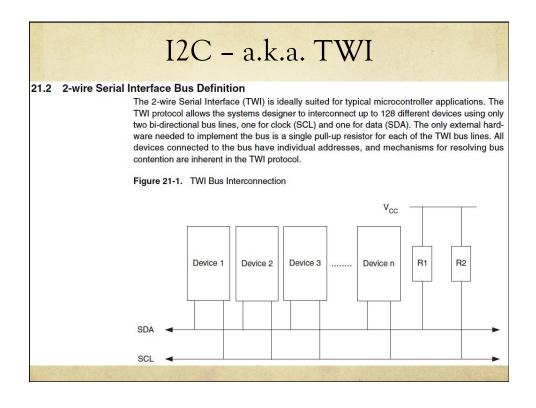
The WCOL bit is set if the SPI Data Register (SPDR) is written during a data transfer. The WCOL bit (and the SPIF bit) are cleared by first reading the SPI Status Register with WCOL set, and then accessing the SPI Data Register.

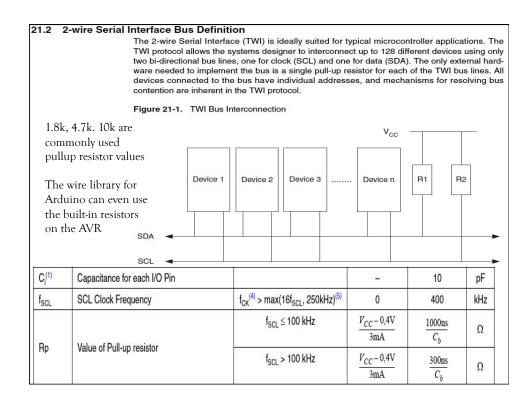
• Bit 0 - SPI2X: Double SPI Speed Bit

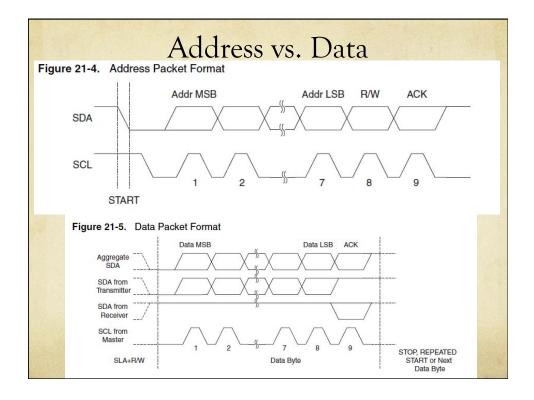
When this bit is written logic one the SPI speed (SCK Frequency) will be doubled when the SPI is in Master mode (see Table 18-5). This means that the minimum SCK period will be two CPU clock periods. When the SPI is configured as Slave, the SPI is only guaranteed to work at $f_{osc}/4$ or lower.

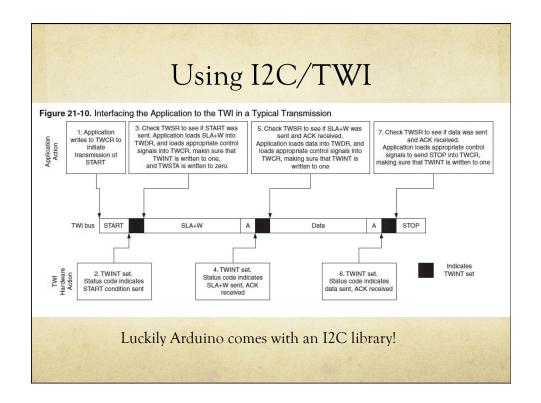






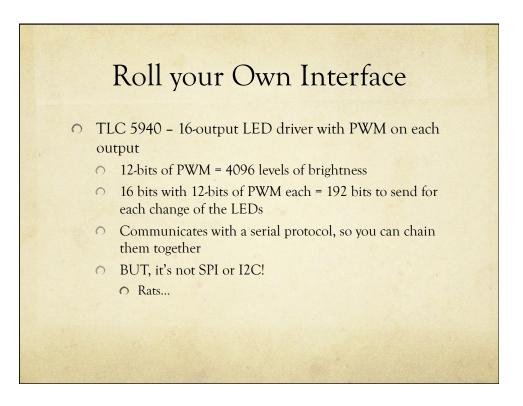


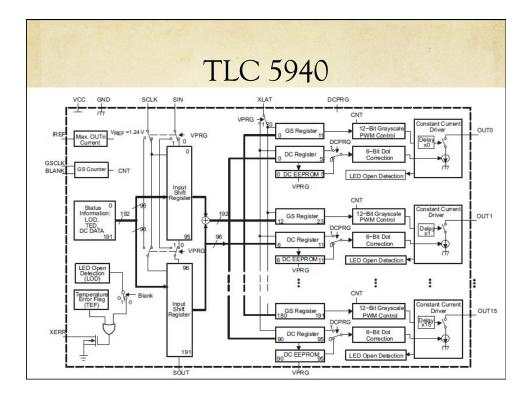


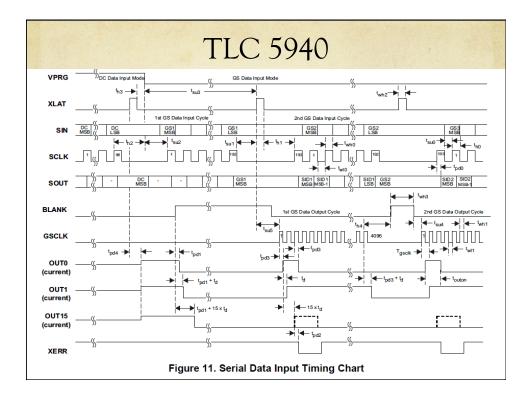


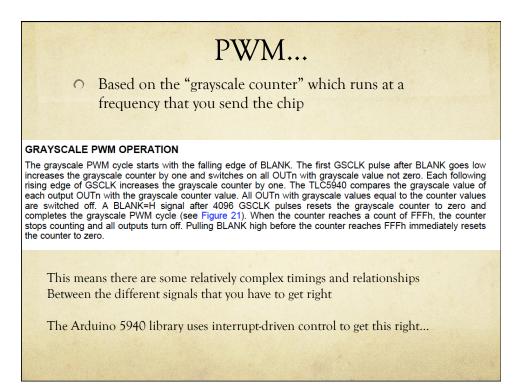


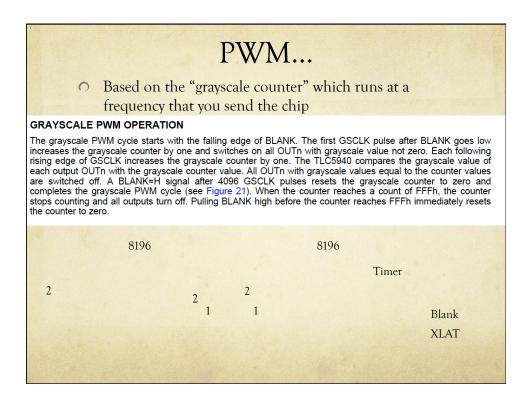
#include <Wire.h> // TWI (I2C) sketch to communicate with the LIS3LV02DQ accelerometer // Using the Wire library (created by Nicholas Zambetti) // On the Arduino board, Analog In 4 is SDA, Analog In 5 is SCL // The Wire class handles the TWI transactions, abstracting the nitty-gritty to make // prototyping easy. void setup(){ pinMode(9, OUTPUT); digitalWrite(9, HIGH); Serial.begin(9600); Wire.begin(); // join i2c bus (address optional for master) Wire.beginTransmission(0x1D); Wire.send(0x20); // CTRL_REG1 (20h) Wire.send(0x87); // Device on, 40hz, normal mode, all axis's enabled Wire.endTransmission(); } // Switch to Wii Nunchuck Slides!







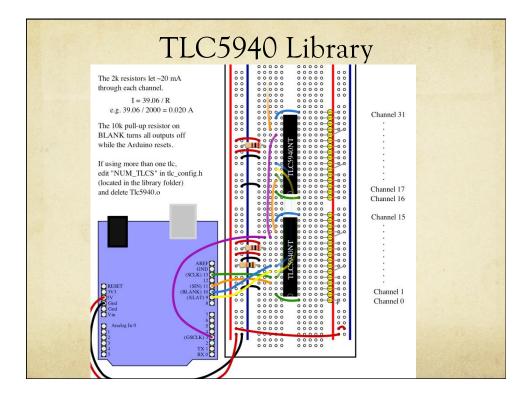


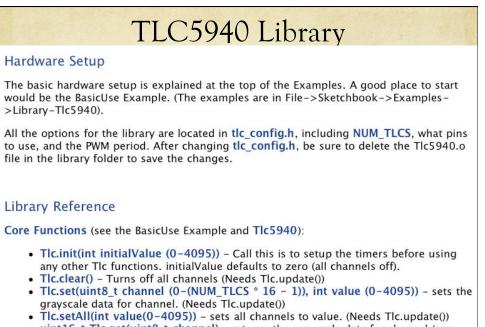


TLC5940 Library

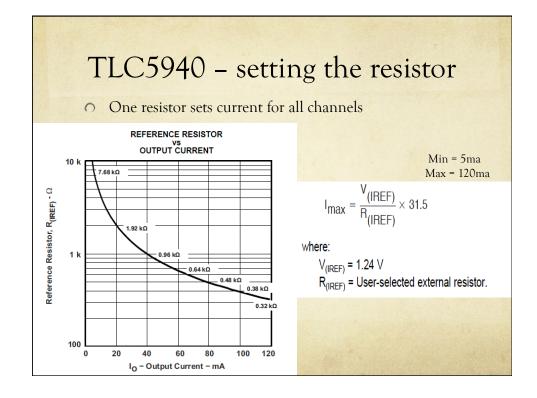
First, for a serial interfaced part it has a rather large number of signals. Fortunately we can ignore many of them if we wish.

- XERR : open collector, wire or-ed output that lets you know a TLC5940 is over heated or has a burnt out LED. We can ignore this as it will always be on unless you have current using elements on all of the outputs.
- SOUT: serial data out from the TLC5940. Unless you wish to try to read the error bits you do not need this to
 come to the Arduino. If you have more than one TLC5940 this is the line you daisy chain to the SIN of the
 next package.
- DCPRG: this selects the source of the current limiter register, you could just tie it high.
- XLAT: you will need this to latch data after shifting.
- SCLK: you will need this to shift data.
- SIN: serial in to TLC5940, this is the output from the Arduino.
- VPRG: you need this to select either the current limit registers or the duty cycle registers for writing.
- GSCLK: this is the clock for the PWM. We will reprogram TIMER2 in the Arduino to make this signal. That will cost us the native PWM on that timer, digital 11 on a mega8, 11 and 3 on a mega168.
- BLANK: this marks the end of a PWM cycle in addition to blanking the output. We will reprogram TIMER1 to
 generate this signal. That will cost us the native PWMs on digital 9 and digital 10. (Tie a real, physical pull-up
 resistor on this line to keep things blanked while your Arduino boots. Depending on your hardware, it is
 possible that the TLC5940 would come up in a configuration that would dissipate too much power.)





- uint16_t Tlc.get(uint8_t channel) returns the grayscale data for channel (see set).
- Tlc.update() Sends the changes from any Tlc.clear's, Tlc.set's, or Tlc.setAll's.



TLC5940 Summary

• Easy to use – if you use the tlc5940 library!

- Can also use for servo control
 - Use the PWM channels to drive servos
 - Remember about power issues!
 - Separate tlc5940 servo library
 - Resets some timing to get the servo timing right...

| TLC servo | functions. More |
|-----------|--|
| | <avr io.h=""></avr> |
| #include | "Tlc5940.h" |
| Go to the | source code of this file. |
| | |
| Defines | |
| #define | SERVO_MAX_ANGLE 180 The maximum angle of the servo. |
| #define | SERVO_MIN_WIDTH 204 The 1ms pulse width for zero degrees (0 - 4095). |
| #define | SERVO_MAX_WIDTH 410 The 2ms pulse width for 180 degrees (0 - 4095). |
| #define | SERVO_TIMER1_TOP 20000 The top value for XLAT and BLANK pulses. |
| #define | SERVO_TIMER2_TOP 77 The top value for GSCLK pulses. |
| Function | s |
| void | tlc_initServos (uint8_t initAngle) Initializes the tlc. |
| void | tlc_setServo (TLC_CHANNEL_TYPE channel, uint8_t angle) Sets a servo on channel to angle. |
| uint8_t | tlc_getServo (TLC_CHANNEL_TYPE channel) Gets the current angle that channel is set to. |
| uint16_t | tlc_angleToVal (uint8_t angle) Converts and angle (0 - SERVO_MAX_ANGLE) to the inverted tlc channel value (4095 - 0). |
| | tic valToAngle (uint16 t value) |

